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HP2-T11

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DEMO EXAM

QUESTION: 1
Which statement best describes PCI Express architecture?

A. Data is sent in parallel
B. Data is sent serially over each link
C. PCI Express has more pins than PCI-X
D. PCI Express transfers data in half-duplex

Answer: B

QUESTION: 2
Which statement is correct when comparing similar versions of Intel Xeon and AMD processors?

A. Xeon processors are optimized for parallel execution
B. Opteron processors are optimized for parallel execution
C. Opteron processors use a Northbridge that operates at core bus speed
D. Xeon processors use an intergraded memory controller that operates at core processor speed

Answer: B

QUESTION: 3
A customer asks you for information about the advantages of RAID 6 over RAID leves. What can you recommend? (Select three)

A. Performance is equivalent to RAID 5 when reading data.
B. It is the most cost-effective of the fault-tolerant RAID solution.
C. It has greater fault-tolerance than RAID4, RAID1 + 0, or RAID 5.
D. It uses the most storage capacity or fault tolerance of all the RAID level.
E. It is ideal for applications requiring large logical drives with many physical drives.

Answer: A, C, E

QUESTION: 4
What is a characteristic of a PCI-X bus?

A. You cannot use PCI-X cards in a conventional PCI system
B. It is fully backward compatible with conventional PCI devices
C. You cannot use existing PCI devices in a PCI-X based system
D. It is compatible with conventional PCI devices only after updating to newer drivers

Answer: B

QUESTION: 5
What is the difference between single-ended SCSI and differential SCSI?

A. Single-ended uses twisted pair wires
B. Differential is more susceptible to noise
C. Differential uses a single wire and has length limitations
D. Single-ended uses a single wire and has length limitations

Answer: D

QUESTION: 6
Which statements apply to AMD 2P or 4P system architecture? (Select three)

A. Each processor can access only its own memory
B. Requests for memory access are handled by Northbridge ASIC
C. Each processor can access the full complement of installed memory
D. Memory must be installed in banks corresponding to the installed processors
E. The maximum amount of memory can be installed, regardless of the number of installed processors
F. Requests for memory access are handled directly by the corresponding processor and relayed thought the Hyper Transport link

Answer: A, D, F

QUESTION: 7
What is a characteristic of Double Data Rate (DDR) RAM?

A. sends data every other clock cycle
B. send data on the rising edge of the clock cycle only
C. send data on the falling edge of the clock cycle only
D. send data on both the rising and falling edge of each cycle

Answer: D

QUESTION: 8
What happens if you install a 66NHz, 32-bit PCI card in a 33 MHz, 64-bit PCI slot? (Select two)

A. The 66MHz card operates at 33MHz
B. The 66MHz, 32-bit card operates at 33MHz 32-bit mode
C. The 66MHz, 32-bit card operates at 33MHz 64-bit mode
D. Any 33MHz, 64-bit card on the PCI operate like 33MHz, 32-bit cards

Answer: A, B

QUESTION: 9
What is an advantage of using iSCSI?

A. it enables the user of SCSI cables for Ethernet communications
B. it enables the use of SAS and SATA drives using standard SCSI controllers
C. it overcomes the inability to use shared storage devices in a heterogeneous environment
D. it overcomes the distance limitations associated with standard Fiber Channel storage transport

Answer: A

QUESTION: 10
In systems with AMD processors, what allows communication between processors and the I/O subsystem?

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