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# HP0-680

**HP**

*Implementing Enterprise Virtual Array*

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**QUESTION 1**

Which Compaq fault-tolerant methodology uses multiple disk controllers?

- A. RAID 5
- B. multiplexing
- C. duple Xing
- D. RAID 1

Answer: C

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**QUESTION 2**

Which Compaq RAID level requires two parity drives?

- A. Distributed Data guarding
- B. Dedicated Parity Drive
- C. Advanced Data Guarding
- D. Distributed Parity

Answer: C

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**QUESTION 3**

When memory is the only component to be tested, which diagnostic option should be select?

- A. Quick Check
- B. Automatic
- C. Prompted
- D. CPQSET+

Answer: C

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**QUESTION 4**

What do online spares enable the server to do in the Advanced Memory Protection system?

- A. fail over to the spare bank.
- B. Protect against a double bit error
- C. Read memory faster
- D. Stripe across DIMMS

Answer: A

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**QUESTION 5**

Where are the Compaq server system interlock LEDs found?

- A. hot-pluggable power supply
- B. hot-pluggable backplane

- C. system I/O board
- D. terminator board

Answer: C

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**QUESTION 6**

Which utility is recommended to be run after replacing a Compaq server I/O device?

- A. system configuration Utility
- B. ERASE Utility
- C. Insight Manager
- D. I/O configuration Utility

Answer: A

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**QUESTION 7**

What I/O device is used to check Compaq server information without using a monitor?

- A. IMD
- B. UID
- C. Parallel Port
- D. USB Port

Answer: A

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**QUESTION 8**

Which PCI buses do Compaq Servers support?

- A. 32 bit and 64 bit
- B. 32 bit and 48 bit
- C. 32 bit, 64 bit and 128 bit
- D. 64 bit and 128 bit

Answer: A

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**QUESTION 9**

What regulated the Pentium III processor power?

- A. processor
- B. system board
- C. processor terminator
- D. processor terminator
- E. processor power module

Answer: D

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**QUESTION 10**

What regulates the Pentium II processor power?

- A. Processor
- B. System board
- C. Processor
- D. Processor Power Module

Answer: D

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**QUESTION 11**

What new Compaq server processor technology eliminates the need for an external Processor power Module?

- A. Eight-way Way Profusion Chip Set
- B. On-Chip voltage Regulation (OCVR)
- C. Symmetric Multiprocessing
- D. Asymmetric Multiprocessing

Answer: B

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**QUESTION 12**

On vraid5, the HSV110 will write one parity segment for every \_\_\_\_\_ data segments.

- A. three
- B. four
- C. five
- D. six

Answer: B

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**QUESTION 13**

What is the minimum number of quorum disks created when the EVA is initialized?

- A. two
- B. four
- C. five
- D. six

Answer: C

---

**QUESTION 14**

You have a disk group with no protection set. It contains three virtual disks: a vraid0, a vraid1, and a vraid5 that takes up half of the available disk space.

- A. All three Vriad0 disks will fail.



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